Deterministic assembly of microdevices by transfer-printing is an advanced manufacturing technology that enables the heterogeneous integration of disparate materials on large-area substrates.\textsuperscript{[1–5]} All active electronic devices generate heat as a byproduct of their operation and thermal management of transfer-printed assemblies must be a consideration whenever the heat flux is large. While the thermal conductivities of most materials used in microelectronics are well known, the thermal conductance of interfaces formed by transfer-printing is unknown. We report studies of the thermal conductance of interfaces formed by transfer-printing of Au and Au(Pd) alloy thin films, 100 μm × 100 μm in area and 100 nm thick, on amorphous SiO\textsubscript{2}, hydrogen-terminated Si(001), and single-crystal Al\textsubscript{2}O\textsubscript{3} substrates. We find that the thermal conductance \( G_t \) of transfer-printed interfaces spans a relatively small range, \( 10 < G_t < 40 \ \text{MWm}^{-2}\text{K}^{-1} \) despite significant differences in the thermal conductivity of the substrates and interfaces roughness. These values of \( G_t \) are smaller than the conductance of interfaces formed by physical vapor deposition but orders of magnitude larger than the conductance of pressed contacts between macroscopic polished surfaces. The relatively small thermal resistance of transfer-printed interfaces will not create a significant thermal management problem in most applications of transfer-printing but will be a concern in high-power technologies that involve extremely large heat fluxes, >10 kW cm\textsuperscript{-2}.

The thermal conductance \( G \) of interfaces formed by physical vapor deposition of metal films on dielectric substrates has been studied extensively. \( G \) is the transport coefficient that relates the heat flux \( J_\text{Q} \) to the temperature drop \( \Delta T \) at an interface, \( J_\text{Q} = G \Delta T \). The observed values span a large range, from a low conductance of \( G \approx 10 \ \text{MWm}^{-2}\text{K}^{-1} \) for Bi deposited on hydrogen-terminated diamond\textsuperscript{[6]} to a high conductance\textsuperscript{[7] of \( G \approx 700 \ \text{MWm}^{-2}\text{K}^{-1} \) for epitaxial TiN/MgO. Often, the thermal conductance of interfaces formed by physical vapor deposition is reasonably well understood based on the mismatch of vibrational densities of states between the two materials that make up the interface. Controversial issues remain, however, including the role of anharmonicity in enhancing heat transfer between highly dissimilar materials\textsuperscript{[8]} and the role of weak interfacial bonding in suppressing heat transfer far below the values predicted by conventional theory.\textsuperscript{[6]}

The thermal conductance of mechanically-joined materials has also been extensively studied because of their great practical importance in a wide variety of engineering systems.\textsuperscript{[9,10]} The “thermal contact conductance” of joints is many orders of magnitude smaller than the thermal conductance of interfaces formed by physical vapor deposition. Low values of thermal contact conductance, \( G \sim 0.1 \ \text{MWm}^{-2}\text{K}^{-1} \), are also observed in micro-mechanical actuators.\textsuperscript{[11,12]} By contrast, little is known about the thermal conductance of interfaces formed by transfer-printing of a thin film on the types of relatively smooth substrates used in device manufacturing. Prior experimental work is limited to data for stiction-failed Si microcantilevers\textsuperscript{[13]} and Au films transferred to GaAs substrates using alkane-dithiols as an adhesion layer.\textsuperscript{[14]} A thin film will elastically deform to accommodate roughness on large lengths scales but interface roughness on short lengths will remain unless a large pressure is applied. The effects of the remaining nanoscale interface roughness on thermal transport have recently been studied theoretically by Persson.\textsuperscript{[15]} This interface roughness suppresses the true area of contact to an extent that is difficult to predict: i.e., the true area of contact between the film and substrate \( A \) is an unknown fraction of the nominal contact area, \( A_0 \),\textsuperscript{[16]}

Our experiments show that even for moderately rough transfer-printed interfaces, rms roughness \( \approx 2 \) nm, the thermal conductance \( G_t \) of a transfer-printed interface is surprisingly large, \( 10 < G_t < 20 \ \text{MWm}^{-2}\text{K}^{-1} \). This interface conductance is only a factor of \( \approx 4 \) smaller than \( G_0 \) for Au sputter-deposited SiO\textsubscript{2}. We hypothesize that water adsorbed on the substrate and film surfaces from the ambient air generates sufficient capillary forces\textsuperscript{[17,18,19]} to create a true area of contact \( A \) that is significant fraction of the nominal area \( A_0 \), and that the observed value of \( G \) reflects the intrinsic thermal conductance of the atomically-bonded bonded interface, \( G \), scaled by the fractional area \( A/A_0 \), \( G_t \approx G(A/A_0) \). Capillarity provides the force needed to pull the film and substrate together and create significant \( A/A_0 \) but the nanoscale water bridges themselves do not dominate the interfacial heat transport.

Our experiments employed two types of metal films, pure Au and a dilute Au(Pd) alloy with 5 at% Pd; and 3 substrates, SiO\textsubscript{2}, sapphire, and hydrogen-terminated Si. (We chose the Au(Pd) alloy films for this study because the addition of a small amount
Pd to Au dramatically improves the optical properties of Au for time-domain thermoreflectance measurements. To prepare Au films for transfer printing, we deposited 100 nm of Au by magnetron sputtering on 1.1 μm thick thermally grown SiO$_2$ as the sacrificial layer. We found that we could not use SiO$_2$ as a sacrificial layer for Au(Pd) alloy films because Au(Pd) films are damaged by an HF solution; instead, we used a 1 μm thick layer of sputter-deposited Cr as the sacrificial layer for Au(Pd). Au(Pd) alloy films were deposited on the Cr sacrificial layer by magnetron sputter deposition from an alloy target.

We characterized the surface morphology of the sacrificial layers and the substrates using atomic force microscopy. We assume that the morphology of the transfer-printed metal film is similar to morphology of the corresponding sacrificial layer. The height-difference correlation function $g(\rho)$ is a real-space, statistical measure$^{21}$ of surface morphology that provides more extensive information about the morphology than a simple measurement of the rms roughness, $g(\rho) = \langle (h_i - h_j)^2 \rangle$, where $h_i$, $h_j$ are the heights of the surface at position $i$ and $j$, respectively; $\rho$ is the distance between surface points $i$ and $j$; and the brackets denote an average over many pairs of surface points $i$ and $j$. When a surface morphology is dominated by a narrow distribution of in-plane length-scales centered at $\xi$, $[g(\rho)]^{1/2}$ increases linearly with $\rho$ for $\rho < \xi$ and is approximately constant and equal to $\sqrt{2}$ times the rms roughness for $\rho > \xi$. The in-plane length scale $\xi$ of the Cr sacrificial layer, and the SiO$_2$ and Al$_2$O$_3$ receiving substrates are $\xi = 30, 10, \text{and} 70$ nm, respectively. The length scale $\xi$ for the Si:H receiving substrate is not as well defined, see Figure 1. On short length scales, $\rho < \xi$, $[g(\rho)]^{1/2}/\rho$ is nearly constant and provides a measure of the rms slope of the surface morphology. $[g(\rho)]^{1/2}/\rho$ for the SiO$_2$ and Al$_2$O$_3$ substrates, and the Cr sacrificial layer are 0.021, 0.016, and 0.12, respectively.

The samples are assembled using the process flow illustrated in Figure 2. We pattern the metal films to a $100 \times 100 \mu$m$^2$ followed by patterning of the sacrificial layer to 100 μm × 120 μm; patterning of photoresist anchors and frame; releasing of the metal film by etching the remaining sacrificial layer. (b) Process flow for transfer-printing.

**Figure 1.** Square root of the height difference correlation function of the surface morphology of the sacrificial layers (amorphous SiO$_2$, Cr) and the receiving substrates (amorphous SiO$_2$, single crystal Al$_2$O$_3$, and hydrogen-terminated Si).
ratio $G_t/G_S$ ranges between $G_t/G_S \approx 0.25$ for $\text{Al}_2\text{O}_3$ receiving substrates and $G_t/G_S \approx 0.1$ for $\text{SiO}_2$ receiving substrates. In the case of Au, $G_t/G_S > 0.5$ for $\text{SiO}_2$ receiving substrates and $G_t/G_S \approx 0.2$ for Si:H receiving substrates.

For sputter-deposited Au and Au(Pd) films, $G_S$ tends to increase with increasing temperature. This observation is counter to the predictions of models for interface thermal conductance that only consider two-phonon scattering processes, i.e., the elastic exchange of energy between the vibrational modes on the two sides of the interface. We have previously argued that this temperature dependence is a signature of three-phonon interactions, i.e., anharmonic interactions between two phonons in a low Debye temperature materials with one phonon in a high Debye temperature substrate. These effects of anharmonicity have also been observed in computer simulations. The reduction in $G_S$ of Au sputter-deposited on $\text{SiO}_2$ with increasing temperature, see Figure 3, is not reversible and is apparently caused by changes in the interface contact area.
We do not observe a dependence of $G_t$ on the existence of polar vibrational modes in the substrates. $\text{Al}_2\text{O}_3$ and $\text{SiO}_2$ have infrared active vibrational modes but Si does not. Thus, mechanisms for heat transport that are based on coupling of electronic excitations in the metals films with the vibrational modes of the substrate$^{[23,24,15]}$ do not appear to be important in our experiments. (We note, however, that this conclusion is difficult to make quantitative because the lack of infrared active modes at long wavelengths does not necessarily exclude the possibility of such coupling on length-scales comparable to atomic spacings.$^{[23]}$ Also, the vibrational states of adsorbed water will couple to the electronic states of the metal film to a degree that is difficult to quantify.) The weak temperature dependence of $G_t$ provides additional evidence that near-field radiative heat transfer$^{[24,15,25]}$ is not significant. The high frequency optical phonons of $\text{SiO}_2$ and $\text{Al}_2\text{O}_3$ that are presumably most important for near-field radiative transport are not fully thermally excited at room temperatures; near-field radiative heat transport involving these modes should therefore increase strongly with temperature.

Having ruled out heat transport by air conduction and near-field electromagnetic radiation, we conclude that $G_t$ is controlled by the vibrational states of the substrate and substrate. Data for transfer-printed films, $G_t$, do not, however, show a systematic dependence on the thermal conductivity of the substrate. (The thermal conductivity of $\text{SiO}_2$, $\Lambda = 1.3 \text{ Wm}^{-1}\text{K}^{-1}$ is a factor 25 times smaller than $\text{Al}_2\text{O}_3$ and a factor of 100 times smaller than Si.) This result is expected if the spatial extent $d$ of the regions of intimate contact between film and substrate are small compared to the Kapitza length, $L_K = \Lambda / G$, of both the substrate and film. If we assume $G = 60 \text{ MWm}^{-1}\text{K}^{-1}$ for a Au/dielectric interface, the smallest value of $L_K$ for our combination of materials is $L_K \approx 20 \text{ nm}$ for $\text{SiO}_2$ receiving substrates. The lateral length scale $\xi$ of the surface morphology provides an approximate upper bound on $d$: for $\text{SiO}_2$, $d < 30 \text{ nm}$ and the upper bound is comparable to $L_K$. If, on the other hand, the finite thermal conductance of material interfaces is neglected, i.e., the limit $L_K = 0$, then the thermal conductance of a rough interface is expected to have a strong dependence on $\Lambda$.$^{[15]}$ The fact that $G_t$ is approximately the same on all three substrates suggests instead that $d$ is comparable to or smaller than $L_K$ for all cases we have studied.

Data in Figures 3, 4, and 5, were collected while heating the samples. In most cases $G_t$ decreases slightly with initial heating to 100 °C and then remains nearly constant up to 300 °C. We attribute these changes in conductance to the presence of liquid water at the interfaces that contributes to heat transfer$^{[23]}$ at room temperature and is mostly removed at elevated temperatures. The small lateral dimensions of the metal films, 100 μm × 100 μm, facilitates the escape of water and the small quantity of volatile organic contaminants$^{[36]}$ that condense on the surfaces during the short elapsed time between preparation of the substrates and transfer-printing the metal film. We do not know how quickly water can escape from the narrow interface but the diffusion time $\tau$ of water on a length scale of $L = 50 \mu m$ provides some insight: $\tau = L^2/(2D)$, where $D = 2 \times 10^{-5} \text{ cm}^2\text{s}^{-1}$ is the diffusion coefficient of water. $\tau \sim 1 \text{ s}$, a time-scale that is a factor of ~1000 shorter the time required to collect the temperature dependent TDTR data. Furthermore, we note that
the vapor pressure of water at 300 °C is nearly 100 atm, and that 300 °C closely approaches the temperature of the critical point of water, 374 °C. At such high temperatures, the surface tension of water is strongly reduced.

Heat transfer between the transfer-printed film and substrate is controlled by the interfacial thermal conductance of the small area of intimate contact. The relatively large values of $G_1/G_\Sigma$ that we observe imply that the true area of contact $A$ is a significant fraction of the nominal contact area $A_\alpha$, $A/ A_\alpha > 0.1$. A detailed discussion of the mechanics needed to quantitatively describe $A/ A_\alpha$ of a rough interface is beyond the scope of our experimental study but we point out that it is unlikely that intrinsic van der Waals attraction and the pressure used to assemble the sample are sufficient to generate a $A/ A_\alpha$ ratio of this magnitude when the film and substrate are elastically stiff. We instead invoke capillary pressure created by adsorbed water to explain the strong forces needed to create large $A/ A_\alpha$. At relative humidities near 50%, the equilibrium coverage of water on gold, hydrophilic silicon dioxide, and sapphire is 1–3 monolayers. (The coverage of adsorbed water on our SiO$_2$ substrates, prepared by heating to 950 °C, is not known but we expect the water coverage to be smaller than the ~1 nm layer reported in Ref. [29]; hydrogen-terminated Si is hydrophobic and the coverage of water is probably even smaller.) Persson has shown that capillary effects are strongest when adsorbed water can fill the empty spaces at the interface. Thus, small rms roughness produces stronger capillary pressure and enhances $A/ A_\alpha$. Small values of the mean slope of the surface will also enhance the true area of contact.

In summary, we observe surprisingly large thermal conductance $G_1$ at interfaces between transfer-printed metal films and a variety of relatively smooth substrates of the type used in device manufacturing. For the combinations of materials we studied, values for $G_1$ are always $>10$% of the value of $G_\Sigma$ for the corresponding interface formed by sputter-deposition. The weak dependence of $G_1$ on the thermal conductivity and infrared optical properties of the substrate show that spreading thermal resistance and near-field radiative heat transfer are not important considerations. A typical value is $G_1 \approx 20$ $\text{MWm}^{-2}\text{K}^{-1}$, equivalent to the thermal conductance of a ~100 nm thick layer of an amorphous dielectric. Thus, while $G_1$ is an important consideration at sub-micron length-scales for devices with extremely high heat flux, $G_1$ will not usually be a limiting factor in the thermal management of more typical electronic devices assembled by transfer-printing.

Experimental Section

Elastomeric microtip stamps were fabricated by casting and curing elastomer PDMS (polydimethylsiloxane) (Sylgard 184, Dow Corning; 5:1 mixture of base to curing agent). Five pyramidal pits were fabricated on a Si(100) wafer by photolithography and anisotropic wet chemical etching. An epoxy (SU-8 50, MicroChem Corp.; 100 nm thick) was spun on the patterned silicon wafer and a square 100 μm × 100 μm hole was formed in the epoxy layer using photolithography. A PDMS precursor was poured into the negative templates composed of the hole in the epoxy layer and the pyramidal pits, cured in an oven for one hour at 70 °C, and peeled from the template to form the finished microtip stamp.

Prior to transfer-printing of the metal films, the SiO$_2$ and Al$_2$O$_3$ receiving substrates are first prepared by rapid thermal annealing at 950 °C to remove volatile contaminants. The hydrogen terminated Si(001) receiving substrates are prepared with treatment with a 7:1 buffered-oxide etch for 20 s.

In TDTR, the time evolution of surface temperature is measured through temperature-dependent changes in the reflectivity, i.e., the thermoreflectance. (The thermoreflectance of Au(Pd) is a factor of ~3 larger than pure Au.) We analyze the ratio of in-phase $V_{\text{out}}(f)$ and out-of-phase $V_{\text{out}}(f)$ variations in the intensity of the reflected probe beam at the modulation frequency $f = 9.8$ MHz of the pump beam as a function of delay time between pump and probe. The wavelength of the mode-locked Ti:sapphire laser is $\lambda = 785$ nm and the 1/e$^2$ radius of both focused beams is 7 μm. We used average pump and probe powers of 90 and 10 mW for Au, and 54 and 26 mW for Au(Pd). Our TDTR approach has been thoroughly validated and extensively applied in studies of the thermal conductivity of thin films and the thermal conductance of interfaces. Typically, the dominate source of experimental uncertainty is the thickness of the metal film; we measure the film thickness by picosecond acoustics. Sensitivity analysis and error propagation are discussed in Refs. [31] and [6].

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